1. Generate instruction trace file from SAM:
	1. Run SAM
	2. In SAM prompt, run the following commands:
		1. **stop**
		2. **mod load analyzer rstracer.so**
		3. **rstrace –o <trace\_file\_prefix>**
		4. **run**

Instructions Trace files will be generated for all the simulated cores. For example, if SAM is run for 2c2t, 4 instruction trace files will be generated with the following names: <trace\_file\_prefix>.cpu0.rz3.gz, <trace\_file\_prefix>.cpu1.rz3.gz, <trace\_file\_prefix>.cpu2.rz3.gz, <trace\_file\_prefix>.cpu3.rz3.gz.

1. Compile the parser program: **CC -g parse\_trace\_file.cc -o parse\_trace\_file**
2. Run the parser : **parse\_trace\_file <trace\_file\_prefix>.cpu<id>.rz3.gz**